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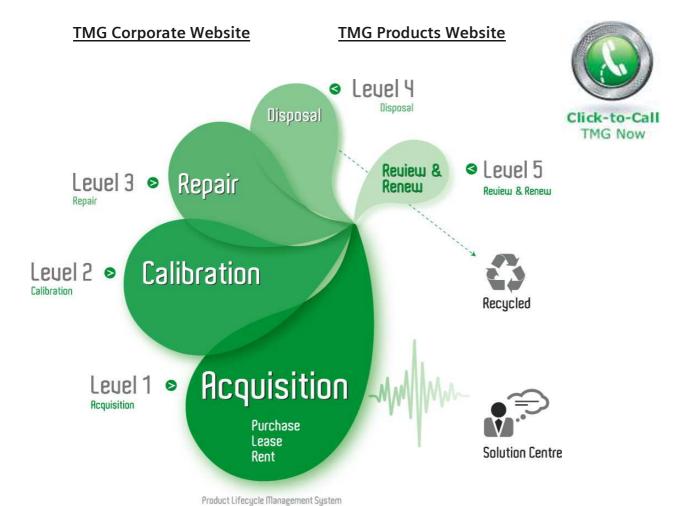
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WavePro® 7 Zi Series

1.5 GHz - 6 GHz



Vertical System	WavePro 715Zi	WavePro 725Zi (SDA)	WavePro 735Zi (SDA, DDA)	WavePro 740Zi (SDA)	WavePro 760Z (SDA, DDA)
Analog (ProLink Input) Bandwidth @ 50 Ω (-3 dB) (≥ 10 mV/div)	Not Applicable	Not Applicable	Not Applicable	4 GHz (≥ 10 mV/div)	6 GHz (≥ 10 mV/div)
Analog (ProBus Input) Bandwidth	1.5 GHz	2.5 GHz	3.5 GHz	3.5 GHz	3.5 GHz
2 50 Ω (-3 dB)	(≥ 10 mV/div)	(≥ 10 mV/div)	(≥ 10 mV/div)	(≥ 10 mV/div)	(≥ 10 mV/div)
nalog (ProBus Input) Bandwidth 1 MΩ (-3 dB)	500 MHz (Typical)	500 MHz (Typical)	500 MHz (Typical)	500 MHz (Typical)	500 MHz (Typical)
Rise Time (Typical, 50 Ω)	235 ps	150 ps	120 ps	105 ps	70 ps
nput Channels	4	100 μο	120 ρο	100 μο	7 0 po
andwidth Limiters	20 MHz, 200	MHz, 1 GHz	20 MHz, 200 MHz 1 GHz, 3 GHz	20 MHz, 200 MHz 1 GHz, 3 GHz	20 MHz, 200 MHz 1 GHz, 3 GHz, 4 GH
nput Impedance	50 Ω +2% or 1 MΩ II	16 pF, 10 MΩ 11 pF \		1 0112, 0 0112	1 0112, 0 0112, 1 01
nput Coupling	1 MΩ: AC, DC, GND;		тип варрноа ргово		
Maximum Input Voltage		50 Ω : ±5 V _{rms} 0 V max. (peak AC: \leq 10) kHz + DC)	50 Ω (ProBus): ±5 V _{rms} 50 Ω (ProLink): ±4 V _{peak} 1 M Ω (ProBus): 250 V max. (peak AC: \leq 10 kHz + DC)	
Channel-Channel Isolation	≥ 100:1 at 2 GHz; ≥ 4	0:1 at 3 GHz; ≥ 20:1 at	4 GHz		
/ertical Resolution		vith enhanced resolution			
Sensitivity	50 Ω: 2 mV-1 V/div, f	ully variable (2–9.99 m\	//div via zoom); 1 MΩ: :	2 mV–10 V/div, fully vai	riable
OC Gain Accuracy	±1.5% of full scale	· · · · · · · · · · · · · · · · · · ·	.,	, ,	
Offset Range	50 Ω (ProBus Input): ±750 mV @ 10–170 mV/div ±4 V @ 172 mV/div-1 V/div 1 MΩ: (ProBus Input): ±1 V @ 2–128 mV/div ±10 V @ 130 mV-1.28 V/div ±100 V @ 1.3 V-10 V/div			50 Ω (ProLink Input): ±750 mV @ 10–118 mV/div ±4 V @ 120 mV/div–1 V/div 50 Ω (ProBus Input): ±750 mV @ 10–170 mV/div ±4 V @ 172 mV/div–1 V/div 1 M Ω : (ProBus Input): ±1 V @ 2–128 mV/div ±10 V @ 130 mV–1.28 V/div ±100 V @ 1.3 V–10 V/div	
Offset Accuracy	±(1.5% of full scale +	1 00/ [[[
onsot Accuracy	±(1.0 % Of Tall Scale 1	1.0% of offset value +	1 mV)		
	±(1.5 % of Tail Scale 1	1.0% of offset value +	1 mV)		
Horizontal System	Internal timebase con	nmon to 4 input channe	els; an external clock m		
Horizontal System	Internal timebase con		els; an external clock m		
Horizontal System Timebases Time/Division Range	Internal timebase con Real time: 20 ps/div-1	nmon to 4 input channe	els; an external clock m 0 ps/div–10 ns/div; Rol		
Horizontal System Timebases Time/Division Range Clock Accuracy	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0	nmon to 4 input channe 000 s/div (RIS mode: 2	els; an external clock m 0 ps/div-10 ns/div; Rol bration)		
Horizontal System imebases ime/Division Range Clock Accuracy ime Interval Accuracy	Internal timebase con Real time: 20 ps/div-1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms	els; an external clock m 0 ps/div-10 ns/div; Rol bration) s)	l mode: up to 1000 s/di	(v)
Industrial System imebases ime/Division Range clock Accuracy ime Interval Accuracy itter Noise Floor	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical)	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical)	els; an external clock m 0 ps/div-10 ns/div; Rol bration) s) 800 fs (Typical)	I mode: up to 1000 s/di	560 fs (Typical)
Independent of the Interpolator of the Interpo	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical)	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms}	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical)	I mode: up to 1000 s/di	(v)
Horizontal System Timebases Time/Division Range Clock Accuracy Time Interval Accuracy Litter Noise Floor Trigger and Interpolator Jitter Channel-Channel Deskew Range	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms} , 100 ms max., each ch	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel	I mode: up to 1000 s/di	560 fs (Typical)
Horizontal System Timebases Time/Division Range Clock Accuracy Time Interval Accuracy Ditter Noise Floor Trigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Timebase Reference (Output)	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms}	els; an external clock m 0 ps/div-10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input	mode: up to 1000 s/di 750 fs (Typical) 1 ps _{rms}	560 fs (Typical)
Imebases Ime/Division Range Clock Accuracy Ime Interval Accuracy Inter Noise Floor Irigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Clock	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 s	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms} , 100 ms max., each ch ance, applied at the rea ance, applied at the rea ance, applied at the rea or 1 MΩ impedance,	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary	750 fs (Typical) 1 ps _{rms} input WP740Zi	560 fs (Typical) (Typical) WP760Zi
Imebases Ime/Division Range Clock Accuracy Ime Interval Accuracy Interval Interval Accuracy Interval Accuracy Interval I	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 Ω	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{ms} , 100 ms max., each chance, applied at the rea	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA)	750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA)	560 fs (Typical) (Typical)
Imebases Ime/Division Range Clock Accuracy Ime Interval Accuracy Interval Interval Accuracy Interval Accuracy Interval I	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 s WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 ps _{rms} , 100 ms max., each ch ance, applied at the rea ance, applied at the rea ance, applied at the rea or 1 MΩ impedance,	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s	750 fs (Typical) 1 ps _{rms} input WP740Zi	560 fs (Typical) (Typical) WP760Zi
Imebases Ime/Division Range Clock Accuracy Ime Interval Accuracy Itter Noise Floor Ingger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Timebase Reference (Output) External Clock Acquisition System Single-Shot Sample Rate/Ch	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 s WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate)	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 psms , 100 ms max., each chance, applied at the rea ance, applied at the rea or 1 MΩ impedance, WP725Zi (SDA)	els; an external clock m 0 ps/div=10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s	750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA) on 2 Ch	560 fs (Typical) (Typical) WP760Zi
imebases ime/Division Range Clock Accuracy ime Interval Accuracy itter Noise Floor rigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Clock Acquisition System Single-Shot Sample Rate/Ch	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 s WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitiv	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 psms , 100 ms max., each chance, applied at the rea ance, applied at the rea ance, applied at the rea (Ω or 1 MΩ impedance, WP725Zi (SDA)	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s	750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA) on 2 Ch on 4 Ch	560 fs (Typical) (Typical) WP760Zi
imebases ime/Division Range Clock Accuracy ime Interval Accuracy itter Noise Floor rigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Clock Acquisition System Fingle-Shot Sample Rate/Ch	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 s WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitiv 1,250,000 waveforms	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 psms , 100 ms max., each chance, applied at the rea ance, applied at the rea or 1 MΩ impedance, WP725Zi (SDA)	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s	750 fs (Typical) 1 ps _{rms} input WP740Zi (SDA) on 2 Ch on 4 Ch	560 fs (Typical) (Typical) WP760Zi
imebases ime/Division Range Clock Accuracy ime Interval Accuracy itter Noise Floor rigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Clock Acquisition System Fingle-Shot Sample Rate/Ch Random Interleaved Sampling (RIS) Maximum Trigger Rate Intersegment Time	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting, 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 g WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitiv 1,250,000 waveforms 800 ns	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 psms , 100 ms max., each chance, applied at the rea ance, applied at the rea ance, applied at the rea (Ω or 1 MΩ impedance, WP725Zi (SDA)	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s	input WP740Zi (SDA) on 2 Ch on 4 Ch	560 fs (Typical) (Typical) WP760Zi (SDA, DDA)
imebases ime/Division Range Clock Accuracy ime Interval Accuracy itter Noise Floor rigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Clock Acquisition System Fingle-Shot Sample Rate/Ch Random Interleaved Sampling (RIS) Maximum Trigger Rate Intersegment Time Maximum Acquisition Memory Points/Ch	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 Ω WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitiv 1,250,000 waveforms 800 ns (4 Ch / 2 Ch)	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 psms , 100 ms max., each chance, applied at the rea ance, applied at the rea ance, applied at the rea (Ω or 1 MΩ impedance, WP725Zi (SDA) e signals (20 ps /div. to s/second (in Sequence)	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s 10 ns/div) Mode, up to 4 channels	input WP740Zi (SDA) on 2 Ch on 4 Ch Number of	560 fs (Typical) (Typical) WP760Zi (SDA, DDA)
Imebases Ime/Division Range Clock Accuracy Ime Interval Accuracy I	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 Ω WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitiv 1,250,000 waveforms 800 ns (4 Ch / 2 Ch)	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 psms , 100 ms max., each chance, applied at the rea ance, applied at the rea ance, applied at the rea (Ω or 1 MΩ impedance, WP725Zi (SDA)	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s 10 ns/div) Mode, up to 4 channels	input WP740Zi (SDA) on 2 Ch on 4 Ch Number of	560 fs (Typical) (Typical) WP760Zi (SDA, DDA)
Imebases Ime/Division Range Clock Accuracy Ime Interval Accuracy I	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 Ω WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitiv 1,250,000 waveforms 800 ns (4 Ch / 2 Ch)	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 psms , 100 ms max., each chance, applied at the rea ance, applied at the rea ance, applied at the rea (Ω or 1 MΩ impedance, WP725Zi (SDA) e signals (20 ps /div. to s/second (in Sequence)	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s 10 ns/div) Mode, up to 4 channels	input WP740Zi (SDA) on 2 Ch on 4 Ch Number of	560 fs (Typical) (Typical) WP760Zi (SDA, DDA)
Horizontal System Timebases Time/Division Range Clock Accuracy Time Interval Accuracy Jitter Noise Floor Trigger and Interpolator Jitter Channel-Channel Deskew Range External Timebase Reference (Input) External Timebase Reference (Output) External Clock Acquisition System Single-Shot Sample Rate/Ch Random Interleaved Sampling (RIS) Maximum Trigger Rate Intersegment Time Maximum Acquisition Memory Points/Ch Standard Memory 5-32 – Memory Option M-64 – Memory Option L-128 – Memory Option	Internal timebase con Real time: 20 ps/div–1 ≤ 1 ppm + (aging of 0 < 0.06 / SR + (clock a 1.5 ps (Typical) 3 ps _{rms} (Typical) ±9 x time/div. setting 10 MHz; 50 Ω impeda 10 MHz; 50 Ω impeda 0.1 Hz–100 MHz, 50 Ω WP715Zi 20 GS/s on 2 Ch 10 GS/s on 4 Ch (Option WPZi-1.5GHZ-4X20GS doubles the sample rate) 200 GS/s for repetitiv 1,250,000 waveforms 800 ns (4 Ch / 2 Ch)	nmon to 4 input channe 000 s/div (RIS mode: 2 .5 ppm/yr from last cali ccuracy* Reading) (rms 1 ps (Typical) 2 psms , 100 ms max., each chance, applied at the rea ance, applied at the rea ance, applied at the rea (Ω or 1 MΩ impedance, WP725Zi (SDA) e signals (20 ps /div. to s/second (in Sequence)	els; an external clock m 0 ps/div–10 ns/div; Rol bration) s) 800 fs (Typical) (Typical) annel r input r output applied at the auxiliary WP735Zi (SDA, DDA) 40 GS/s 20 GS/s 10 ns/div) Mode, up to 4 channels	input WP740Zi (SDA) on 2 Ch on 4 Ch Number of	560 fs (Typical) (Typical) WP760Zi (SDA, DDA)

Acquisition Processing	WavePro 715Zi	WavePro 725Zi (SDA)	WavePro 735Zi (SDA, DDA)	WavePro 740Zi (SDA)	WavePro 760Zi (SDA, DDA)	
Averaging	Summed averaging to 1 million sweeps; continuous averaging to 1 million sweeps					
Enhanced Resolution (ERES)	From 8.5 to 11 bits ve	rtical resolution		•		
Envelope (Extrema)	Envelope, floor, or roof for up to 1 million sweeps					
Interpolation	Linear or Sin x/x		'			
Triggering System						
Modes	Normal, Auto, Single,	and Stop				
Sources	Any input channel, Au	x, Aux/10, or line; slop	e and level unique to e	ach source (except line	trigger)	
Coupling Mode	DC, AC, HFRei, LFRei		•			
Pre-trigger Delay	0-100% of memory s	ize (adjustable in 1% ir	ncrements of 100 ns)			
Post-trigger Delay			d at slower time/div set	ttings or in roll mode		
Hold-off by Time or Events		or from 1 to 99,999,99		<u> </u>		
Internal Trigger Range	±4.1 div from center		0 0 0 0 1 1 1 0 1			
Trigger Sensitivity with Edge Trigger (Ch 1–4) ProBus Inputs	2 div @ < 1 GHz 1.5 div @ < 500 MHz 1.0 div @ < 200 MHz (for DC, AC, LFRej coupling, ≥ 10 mV/div, 50 Ω)	2 div @ < 2.5 GHz 1.5 div @ < 1.25 GHz 1.0 div @ < 200 MHz (for DC, AC, LFRej coupling, ≥ 10 mV/div, 50 Ω)	(for DC, AC	2 div @ < 3.5 GHz 1.5 div @ < 1.75 GHz 1.0 div @ < 200 MHz , LFRej coupling, ≥ 10 m	V/div, 50 Ω)	
Trigger Sensitivity with Edge Trigger (Ch 1–4) ProLink Inputs	2 10 1117) div, 30 32 7	Not Applicable		2 div @ < 4 GHz 1.5 div @ < 2 GHz 1.0 div @ < 200 MHz (for DC, AC, LFRej coupling, ≥ 10 mV/div, 50 Ω)	2 div @ < 6 GHz 1.5 div @ < 3 GHz 1.0 div @ < 200 MH: (for DC, AC, LFRej coupling, ≥ 10 mV/div, 50 Ω)	
External Trigger Sensitivity, (Edge Trigger)	2 div @ < 1 GHz 1.5 div @ < 500 MHz 1.0 div @ < 200 MHz (for DC, AC, LFRej coupling)					
Max. Trigger Frequency, SMART Trigger™	1.0 GHz @ ≥ 10 mV/div (minimum triggerable width 500 ps)	2.0 GHz @ ≥ 10 mV/div (minimum triggerable width 300 ps)	2.0 GHz @ ≥ 10 mV/div (minimum triggerable width 250 ps)	2.0 GHz @ a (minimum triggera		
External Trigger Input Range	Aux (±0.4 V); Aux/10 (±					
Basic Triggers						
Edge	Triggers when signal m	eets slope (positive, ne	gative, or either) and leve	el condition.		
TV-Composite Video	Triggers NTSC or PAL with selectable line and field; HDTV (720p, 1080i, 1080p) with selectable frame rate (50 or 60 Hz) and Line; or CUSTOM with selectable Fields (1–8), Lines (up to 2000), Frame Rates (25, 30, 50, or 60 Hz), Interlacing (1:1, 2:1, 4:1, 8:1), or Synch Pulse Slope (Positive or Negative).					
Window	Trigger when signal or	exits a window define	ed by adjustable thresh	olds.		
SMART Triggers						
State or Edge Qualified	Triggers on any input source only if a defined state or edge occurred on another input source. Delay between sources is selectable by time or events.					
Qualified First	In Sequence acquisition mode, triggers repeatably on event B only if a defined pattern, state, or edge (event A) is satisfied in the first segment of the acquisition. Delay between sources is selectable by time or events.					
Dropout			elected time between 1			
Pattern		igh, low, or don't care.		and external trigger inpel can be selected indep		
SMART Triggers with Exclusion To Glitch		r nogativo glitabas with	widths salastable as	ow as 200 ps (dependi	ng an acailleacans	
GIIGI		negative glitches with on intermittent faults.		ovv as zoo ps (dependi	ig on oscilloscope	
Width (Signal or Pattern)	Triggers on positive, negative or both widths with widths selectable as low as 200 ps (depending on oscilloscop bandwidth) to 20 s, or on intermittent faults.					
Interval (Signal or Pattern)		electable between 1 n		rrad an another saves		
Timeout (State/Edge Qualified)	Triggers on any source if a given state (or transition edge) has occurred on another source. Delay between sources is 1 ns to 20 s, or 1 to 99,999,999 events.					
Runt	Trigger on positive or negative runts defined by two voltage limits and two time limits. Select between 1 ns and 20 ns.					
Slew Rate	Trigger on edge rates. Select limits for dV, dt, and slope. Select edge limits between 1 ns and 20 ns.					
Exclusion Triggering	Trigger on intermitten	t faults by specifying th	e expected behavior ar	nd triggering when that	condition is not met.	

High-speed Serial Protocol Triggering	WavePro 715Zi	WavePro 725Zi (SDA)	WavePro 735Zi (SDA, DDA)	WavePro 740Zi (SDA)	WavePro 760Zi (SDA, DDA)
Data Rates	Not available		SPT, standard with b/s–1.25 Gb/s		HSPT, standard with Mb/s–2.7 Gb/s
Pattern Length	_			NRZ or 8b10b	
Clock and Data Outputs	_			ical), AC coupled	
Clock Recovery Jitter	_	1 ps rms + 0.3% l	Jnit Interval rms for PR		50% transition density
Hardware Clock Recovery Loop BW	-		BW = Fbaud/5500, 50		
Low-speed Serial Protocol Triggering (Optional)					
Available		OP), UART-RS232, CA datasheets for comple			
Color Waveform Display					
Type			with high resolution t	ouch screen	
Resolution	WXGA; 1280 x 768 p				
Number of Traces	Display a maximum o	of 8 traces. Simultaneo	usly display channel, z	oom, memory and ma	ith traces.
Grid Styles	Auto, Single, Dual, Q	uad, Octal, X-Y, Single-	+X-Y, Dual+X-Y		
Waveform Representation	Sample dots joined, o				
Integrated Second Display					
Туре	Color 15.3" flat panel	TFT-Active Matrix LCD	with high resolution t	ouch screen	
Resolution	WXGA; 1280 x 768 p		<u> </u>		
LeCroy WaveStream Fast Viewing Mode					
Intensity	256 Intensity Levels	1–100% adjustable via	a front panel control		
Number of Channels	Up to 4 simultaneous		a monte parior control		
Type	Select analog or color				
Max. Sampling Rate			ıt WPZi-1.5GHZ-4X200	S ontion)	
Persistence Aging	Select from 500 ms t		11 11 21 1.30112 4/1200	10 Option)	
Waveforms/Second (continuous)	Up to 2500 Waveforn				
Analog Persistence Display	op to 2000 vvaveron	no, occorra			
Analog and Color-Graded Persistence	Variable saturation lev	els; stores each trace	's persistence data in r	memory	
Persistence Types	Select analog, color, o				
Trace Selection	Activate persistence	on all or any combinati	on of traces		
Persistence Aging	Select from 500 ms t				
Sweep Display Modes		l accumulated with las	st trace highlighted		
High-speed Digitizer Output (Opt	tion)				
Туре	LeCroy LSIB				
Transfer Rate	Up to 250 Mpts/s (Ma	aximum)			
Output Protocol		lanes utilized for data	transfer)		
Control Protocol	TCP/IP				
Command Set	Via Windows Automa	ition, or via LeCroy Re	mote Command Set		
Zoom Expansion Traces					
	Display up to 4 Zoom	and 8 Math/Zoom trac	ces		
Processor/CPU					
Туре	Intel® Core™ 2 Quad, 2	2.5 GHz (or better)			
Processor Memory	2 GB standard, up to	8 GB optional	-t	"1 100"	
Operating Cust			standard with "M-64"	or "L-128" memory)	
Operating System Real Time Clock		Vista® Business Edition red with waveform and			
		chronize to precision in			
Internal Waveform Memory					
			store 16-bit/point full		
Setup Storage) store 16-bit/point full f files limited only by th		capacity.

Interface	WavePro 725Zi WavePro 715Zi (SDA)	WavePro 735Zi (SDA, DDA)	WavePro 740Zi (SDA)	WavePro 760Zi (SDA, DDA)			
Remote Control	Via Windows Automation, or via LeCroy Remote Command Set						
Network Communication Standard	LXI Class C, VXI-11 and VICP						
GPIB Port (Optional)	Supports IEEE – 488.2						
_SIB Port (Optional)	Supports PCI Express Gen1 x4 protocol	with LeCroy supplied AF	기				
Ethernet Port	Supports 10/100/1000BaseT Ethernet in	erface (RJ45 port)					
JSB Ports	Minimum 6 total (Including 3 front panel)	USB 2.0 ports support	Windows compatible of	devices			
External Monitor Port	15-pin D-Type WXGA compatible to supp support LeCroy WPZi-EXTDISP-15 additi- desktop operation with optional LeCroy of	onal touch screen displa	y accessory. Includes :				
Peripheral Bus	LeCroy LBUS standard		•				
Auxiliary Input							
Signal Types	Select External Trigger or External Clock	Input on the front panel					
Coupling	50 Ω: DC; 1 MΩ: AC, DC, GND	2111 50					
Max. Input Voltage	50 Ω : 5 V _{rms} ; 1 M Ω : 250 V (Peak AC < 10) kHz + DC)					
Auxiliary Output							
Signal Types	Select from calibrator, control signals or						
Calibrator Signal	500 Hz–5 MHz square wave or DC level;		2 (0–1 V into 1 MΩ)				
Control Signals	Trigger enabled, trigger out, pass/fail stat	us					
Automatic Setup							
Auto Setup	Automatically sets timebase, trigger, and	sensitivity to display a	wide range of repetitiv	e signals			
Find Vertical Scale	Automatically sets the vertical sensitivity with the maximum dynamic range	and offset for the selec	cted channel to display	a waveform			
General							
Auto Calibration	Ensures specified DC and timing accuracy	cy is maintained for 1 ye	ar minimum.				
Probes							
Probes	Qty. (4) ÷10 Passive Probes						
Probe System	ProBus (and ProLink on 4 and 6 GHz mod	lels). Automatically dete	cts and supports a vari	ety			
	of compatible probes						
Scale Factors	Automatically or manually selected depe						
Calibration Output	1 kHz square wave, 1 V _{p-p} (typical), outpu	it to probe hook					
Power Requirements							
Voltage	100-240 VAC ±10% at 50/60 Hz; 100-12	20 VAC ±10% at 400 Hz	; Automatic AC Voltage	e Selection			
Max. Power Consumption	800 W/ 800 VA						
Environmental							
Temperature (Operating)	+5 °C to +40 °C including CD-RW/DVD-F	KUIVI drive					
Temperature (Non-Operating)	-20 °C to +60 °C 5% to 80% relative humidity (non-conde	noing) up to : 21 °C					
Humidity (Operating)	Upper limit derates to 50% relative humi		t +40 °C				
Humidity (Non-Operating)	5% to 95% relative humidity (non-conde						
Altitude (Operating)	Up to 10,000 ft. (3048 m) at or below +2		200001				
Altitude (Non-Operating)	Up to 40,000 ft. (12,192 m)	. -					
Random Vibration (Operating)	0.5 g _{rms} 5 Hz to 500 Hz, 15 minutes in ea	ach of three orthogonal	axes				
Random Vibration (Non-Operating)	2.4 g _{rms} 5 Hz to 500 Hz, 15 minutes in ea 20 g peak, half sine, 11 ms pulse, 3 shock	ach of three orthogonal	axes as tested per MIL				
differential SHOCK	total as tested per MIL-PRF-28800F	s (positive and negative)	in each of three offroge	orial axes, TO SHOCKS			
Physical Dimensions							
Dimensions (HWD)	355 mm x 467 mm x 289 mm; 14" x 18.4"	x 11.4" (height excludes	feet)				
Weight Shipping Weight	18.4 kg; 40 lbs. 26.6 kg; 58 lbs.						
Certifications	CE Compliant, UL and cUL listed; confor	ms to FN 61326 FN 61	010-1 UL 61010 2nd a	dition and			
	CSA C22.2 No. 61010-1-04	113 to LIN 01320, LIN 011	010 1, 02 01010 2110 e	andon, and			
Warranty and Service							
	3-year warranty; calibration recommende upgrades, and calibration services.	ed annually. Optional ser	vice programs include	extended warranty,			

Standard

Math Tools

Display up to 8 math function traces (F1–F8). The easy-to-use graphical interface simplifies setup of up to two operations on each function trace, and function traces can be chained together to perform math-on-math.

absolute value invert (negate)
average (summed) log (base e)
average (continuous) log (base 10)
derivative product (x)
deskew (resample) ratio (/)
difference (–) reciprocal
enhanced resolution (to 11 bits vertical) rescale (with units)

envelope roof
exp (base e) (sinx)/x
exp (base 10) square
fft (power spectrum, magnitude, phase,
up to 128 Mpts) sum (+)
floor zoom (identity)

integral

- Parameter math add, subtract, multiply, or divide two different parameters
- Narrow-band power measurements
- Auto-correlation function
- Sparse function
- Cubic and Quadratic Interpolation function

Measure Tools

Display any 12 parameters together with statistics, including their average, high, low, and standard deviations. Histicons provide a fast, dynamic view of parameters and wave shape characteristics.

level@x rms amplitude maximum std. deviation area base mean ton cycles median width data minimum median narrow band phase phase delay narrow band power time @ minimum (min.) ∆ delay number of points time @ maximum (max.) duty cycle Δ time @ level duration +overshoot falltime (90-10%, -overshoot Δ time @ level from 80-20%, @ level) trigger peak-to-peak frequency x@ max. period

x@ min.

Pass/Fail Testing

first

last

Simultaneously test multiple parameters against selectable parameter limits or pre-defined masks. Pass or fail conditions can initiate actions including document to local or networked files, e-mail the image of the failure, save waveforms, send a pulse out at the front panel auxiliary BNC output, or (with the GPIB option) send a GPIB SRQ.

risetime (10-90%.

20-80%, @ level)

Standard

Jitter and Timing

Parametric Measurements:

- period@level width@level duty@level frequency@level
- TIE@level edge@level

Statistical Analysis:

Jitter Trend (1000 pts) • Histograms (1000 pts)

Software Options

Jitter and Timing Analysis Software Package (WPZi-JTA2)

This package provides jitter timing and analysis using time, frequency, and statistical views for common timing parameters, and also includes other useful tools. JTA2 includes:

- Jitter and timing parameters, with "Track" graphs of
- Cycle-Cycle Jitter
 N-Cycle
 N-Cycle with start selection
 Frequency
 Period
 Half Period
 Skew
 Duty Cycle
 Duty Cycle Error
 Full Period
 Skew
 Duty Cycle
 Duty Cycle Error
 Setup
- Edge@lv parameter (counts edges)
- Histograms expanded with 19 histogram parameters and up to 2 billion events
- Trend (datalog) of up to 1 million events
- Track graphs of all parameters
- Persistence histogram, persistence trace (mean, range, sigma)

Spectrum Analyzer Mode (WPZi-SPECTRUM)

This package provides a new capability to navigate waveforms in the frequency domain using spectrum analyzer type controls.

FFT capability added to include:

- power averaging power density real and imaginary components
- frequency domain parameters FFT on up to 128 Mpts.

Disk Drive Measurements Package (WPZi-DDM2)

This package provides disk drive parameter measurements and related mathematical functions for performing disk drive WaveShape Analysis.

• Disk Drive Parameters are as follows:

amplitude assymetry local time trough-peak local base local time under threshold local baseline separation narrow band phase local maximum narrow band power local minimum overwrite pulse width 50 local number local peak-peak pulse width 50local time between events pulse width 50+ local time between peaks resolution local time between troughs track average amplitude local time at minimum track average amplitudetrack average amplitude+ local time at maximum local time peak-trough auto-correlation s/n local time over threshold non-linear transition shift

ORDERING INFORMATION

Product Description	Product Code	Product Description	Product Code
WavePro 7 Zi Series Oscilloscopes		Memory and Sample Rate Options (cont'd)	
1.5 GHz, 10 GS/s, 4 Ch, 10 Mpts/Ch (20 GS/s and 20 Mpts/Ch in interleaved mode) with 50 Ω and 1 MΩ Input	WavePro 715Zi	64 Mpts/Ch (128 Mpts/Ch Interleaved) Memory Option for WavePro 7 Zi. Includes an additional 6 GB of RAM (8 GB total)	WPZi-M-64
2.5 GHz, 20 GS/s, 4 Ch, 10 Mpts/Ch (40 GS/s and 20 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	WavePro 725Zi	64 Mpts/Ch (128 Mpts/Ch Interleaved) Memory Option for DDA 7 Zi. Includes an additional 6 GB of RAM (8 GB total)	DDAZi-M-64
3.5 GHz, 20 GS/s, 4 Ch, 10 Mpts/Ch (40 GS/s and 20 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	WavePro 735Zi	64 Mpts/Ch (128 Mpts/Ch Interleaved) Memory Option for SDA7 Zi. Includes an additional 6 GB of RAM (8 GB total)	SDAZi-M-64
4 GHz, 20 GS/s, 4 Ch, 10 Mpts/Ch (40 GS/s and 20 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	WavePro 740Zi	128 Mpts/Ch (256 Mpts/Ch Interleaved) Memory Optic for WavePro 7 Zi. Includes an additional 6 GB of RAM (8 GB total)	on WPZi-L-128
6 GHz, 20 GS/s, 4 Ch, 10 Mpts/Ch (40 GS/s and 20 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	WavePro 760Zi	128 Mpts/Ch (256 Mpts/Ch Interleaved) Memory Optic for DDA 7 Zi. Includes an additional 6 GB of RAM (8 GB total)	on DDAZi-L-128
SDA Zi Series Serial Data Analyzers		128 Mpts/Ch (256 Mpts/Ch Interleaved) Memory Optic for SDA 7 Zi. Includes an additional 6 GB of RAM	on SDAPZi-L-128
2.5 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch (40 GS/s and 40 Mpts/Ch in interleaved mode) with 50 Ω and 1 M Ω Input	SDA 725Zi	(8 GB total) 20 GS/s (40 GS/s Interleaved) Sampling Rate V Option for 1.5 GHz WavePro 715 Zi	/PZi-1.5GHZ-4X20GS
3.5 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch (40 GS/s and 40 Mpts/Ch in interleaved mode)	SDA 735Zi	CPU, Computer and Other Hardware Options	
with 50 Ω and 1 MΩ Input			/PZi-2-UPG-8GBRAM
4 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch (40 GS/s and 40 Mpts/Ch in interleaved mode)	SDA 740Zi		/PZi-4-UPG-8GBRAM
with 50 Ω and 1 $M\Omega$ Input	CD 4 7007:	Upgrade from Standard Size Hard Drive to 200 GB Hard Drive	WPZi-200GB-HD
6 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch (40 GS/s and 40 Mpts/Ch in interleaved mode)	SDA 760Zi	Additional 80 GB Hard Drive	WPZi-80GB-RHD-02
with 50 Ω and 1 M Ω Input		Additional 200 GB Hard Drive	NPZi-200GB-RHD-02
·		GPIB Option for LeCroy Oscilloscope	GPIB-2
DDA 7 Zi Series Oscilloscopes 3.5 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch	DDA 735Zi	Sovial Data Ontions and Assessavias	
(40 GS/s and 20 Mpts/Ch in interleaved mode)	DDA 730ZI	Serial Data Options and Accessories 2.7 Gb/s High-speed Serial Pattern Trigger Option	WPZi-HSPT
with 50 Ω and 1 M Ω Input 6 GHz, 20 GS/s, 4 Ch, 20 Mpts/Ch	DDA 760Zi	for 4–6 GHz Oscilloscopes (Standard on SDA 7 Zi	VVPZI-HSPT
(40 GS/s and 20 Mpts/Ch in interleaved mode)	DDA 70021	and DDA 7 Zi) 1.25 Gb/s Medium-speed Serial Pattern Trigger Option	WPZi-MSPT
with 50 Ω and 1 M Ω Input		for 2.5–3.5 GHz Oscilloscopes (Standard on SDA 7 Zi and DDA 7 Zi)	VVFZI-IVISF I
Included with Standard Configuration ÷10, 500 MHz Passive Probe (Oty. 4)			PZi–CBL-DE-EMBED
ProLink to SMA Adapter: 4 each	LPA-SMA-A	8b10b Decode only Option (Standard on SDA 7 Zi	WPZi-8B10B D
Optical 3-Button Wheel Mouse, USB 2.0		and DDA 7 Zi)	VVI 21051055
Protective Front Cover		I ² C Bus Trigger and Decode Option	WPZi-I2Cbus TD
Printed Quick Reference Guide		SPI Bus Trigger and Decode Option	WPZi-SPIbus TD
Printed Getting Started Manual		LIN Trigger and Decode Option	WPZi-LINbus TD
Product Manual Set on CD-ROM			-UART-RS232bus TD
Norton Anti-virus Software (Trial Version)			WPZi-FlexRayBus TD
Microsoft Windows® Vista® License Commercial NIST Calibration with Performance Certifica	nte	FlexRay Bus Trigger, Decode, and Physical Layer W Test Option	/PZi-FlexRayBus TDP
Power Cable for the Destination Country		CANbus TDM Trigger, Decode and	WPZi-CANbus TDM
3-year Warranty		Measure/Graph Option CANbus TD Trigger and Decode Option	WPZi-CANbus TD
Memory and Sample Rate Options		Ethernet Application Software	QPHY-ENET*
32 Mpts/Ch (64 Mpts/Ch Interleaved) Memory Option	WPZi-S-32	USB Application Software	QPHY-USB [†]
for WavePro 7 Zi. Includes an additional 2 GB of RAM		PCle Gen1 Compliance and Development Software Pa	ckage QPHY-PCle
(4 GB total) 32 Mpts/Ch (64 Mpts/Ch Interleaved) Memory Option	DDAPZi-S-32	QualiPHY Enabled SATA Software Option	QPHY-SATA
for DDA 7 Zi. Includes an additional 2 GB of RAM	DDAFZI-3-3Z	WiMedia UWB Transmitter Measurement Software Op	
(4 GB total)	SD 47; C 22	Eye Doctor (Virtual Probe and Equalizer emulation) Bun	
32 Mpts/Ch (64 Mpts/Ch Interleaved) Memory Option for SDA 7 Zi. Includes an additional 2 GB of RAM (4 GB total)	SDAZi-S-32	Eye Doctor Virtual Probing Element Eye Doctor Equalized Receiver Emulation	WPZi-EYEDR-VP WPZi-EYEDR-EQ
(4 OD (Otal)		*TF-ENET-B required. [†] TF-USB-B required.	

ORDERING INFORMATION

Product Description	Product Code	Product Description Pro	duct Code
High-speed Digitizer Output		Probes and Probe Accessories	
High-speed PCIe Gen1 x4 Digitizer Output	LSIB-1	2.5 GHz, 0.7 pF Active Probe (÷10), Small Form Factor	HFP2500
PCI Express X4 Host Interface Board for Desktop PC	LSIB-HOSTBOARD	1.5 GHz, 0.9 pF, 1 M Ω High Impedance Active Probe	ZS1500
PCI Express X4 Express Card	LSIB-HOSTCARD		00-QUADPAK
Host Interface for Laptop Express Card Slot	20.5 1.0010, 11.5	High Impedance Active Probe	00 Q0ADI AK
PCI Express X4 3-meter Cable with X4 Cable	LSIB-CABLE-3M	WaveLink 7.5 GHz, Differential Probe Adjustable Tip Module	D600A-AT*
Connectors Included PCI Express X4 7-meter Cable with X4 Cable	LCID CADLE 7M	WaveLink 3.5 GHz, 2.5 V _{p-p} Differential Probe	D310*
Connectors Included	LSIB-CABLE-7M	Small Tip Module	
Mixed Signal Testing Options		WaveLink 3.5 GHz, 5 V _{p-p} Differential Probe Small Tip Module	D320*
500 MHz, 2 GS/s, 18 Ch, 50 Mpts/Ch	MS-500	WaveLink 6 GHz, 2.5 V _{p-p} Differential Probe Small Tip Module	D610*
Mixed Signal Oscilloscope Option	140 500 00	WaveLink 6 GHz, 5 V _{p-p} Differential Probe	D620*
250 MHz, 1 GS/s, 36 Ch, 25 Mpts/Ch	MS-500-36	Small Tip Module	2020
(500 MHz, 18 Ch, 2 GS/s, 50 Mpts/Ch Interleaved) Mixed Signal Oscilloscope Option		WaveLink 6 GHz, Differential Positioner Mounted Tip Module	D500PT*
250 MHz, 1 GS/s, 18 Ch, 10 Mpts/Ch	MS-250	WaveLink ProLink Probe Body	WL-PLink
Mixed Signal Oscilloscope Option	1110 200	WaveLink ProBus Probe Body	WL-PBus
, ,		7.5 GHz Low Capacitance Passive Probe (÷10, 1 kΩ; ÷20, 500	
General Purpose and Application Specific		1 GHz, Active Differential Probe (÷1, ÷10, ÷20)	AP034
Software Options		Optical-to-Electrical Converter, 500–870 nm ProLink	OE525
Advanced Customization Software Package	WPZi-XDEV	BMA Connector	02020
Spectrum Analyzer and Advanced FFT Option	WPZi-SPECTRUM	Optical-to-Electrical Converter, 950–1630 nm ProLink	OE555
EMC Pulse Parameter Software Package	WPZi-EMC	BMA Connector	
Serial Data Mask Software Package	WPZi-SDM	10/100/1000Base-T Compliance Test Fixture	TF-ENET-B [†]
(Standard on SDA 7 Zi and DDA 7 Zi)		Telecom Adapter Kit 100 Ω Bal., 120 Ω Bal., 75 Ω Unbal.	TF-ET
Advanced Optical Recording Measurement Package	WPZi-AORM	SATA Gen1/Gen2 Compliance Test Fixture	TF-SATA
Demodulation Software Package	WPZi-DMOD	USB 2.0 Testing Compliance Test Fixture	TF-USB-B
Jitter Timing and Analysis Software Package (Standard on SDA7 Zi and DDA 7 Zi)	WPZi-JTA2	* For a complete probe, order a W-PLink or WL-PBus Probe Body with the Probe Tip Module	
Digital Filter Software Package	WPZi-DFP2	·	
Disk Drive Measurements Software Package (Standard on DDA 7 Zi)	WPZi-DDM2	[†] Includes ENET-2CAB-SMA018 and ENET-2ADA-BNCSMA	
Electrical Telecom Mask Test Software Package	WPZi-ET-PMT	A variety of other active voltage and current probes are also Consult LeCroy for more information.	available.
General Accessories			
Top-mounted, Fully Integrated 15.3" WXGA with	WPZi-EXTDISP-15	Customer Service	
Touch Screen Display, Including all Cabling and Softwa		LeCroy oscilloscopes and probes are designed, built, and tes	
Keyboard, USB	KYBD-1	ensure high reliability. In the unlikely event you experience o	
Probe Deskew and Calibration Test Fixture	TF-DSQ	our digital oscilloscopes are fully warranted for three years a probes are warranted for one year.	na our
Hard Carrying Case	WPZi-HARDCASE	,	
Soft Carrying Case	WPZi-SOFTCASE	This warranty includes:	
Rackmount Accessory for Converting a Zi Series Oscilloscope to an 8U Rack-mounted Package	RACKMOUNT-1	No charge for return shipping Long torm 7 year support	
ProLink to SMA Adapter	LPA-SMA-A	Long-term 7-year support	
Kit of Dool into to CNAA Advantage	L DA CNAA KIT A	 Upgrade to latest software at no charge 	

LPA-SMA-KIT-A

OC1024

OC1021



Oscilloscope Cart

Kit of ProLink to SMA Adapters

Oscilloscope Cart with Additional Shelf and Drawer

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