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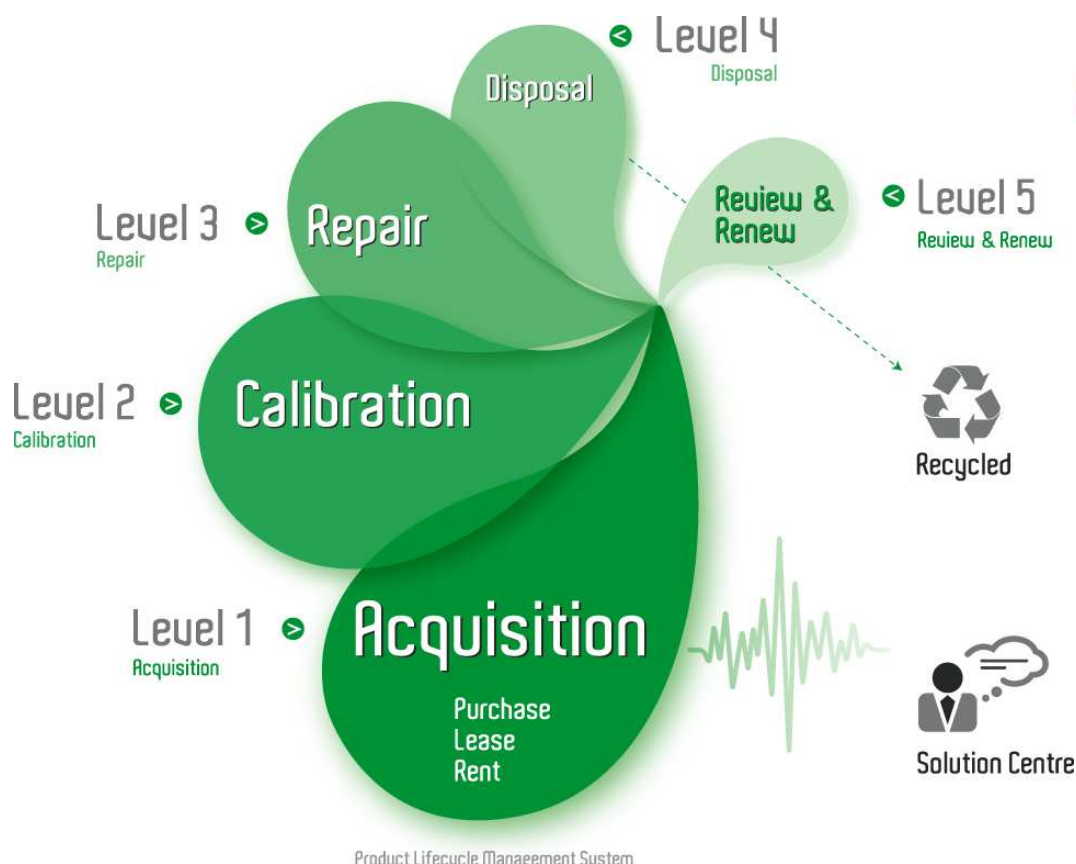
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PatternSync Trigger Module for the DSA8200*¹ Series Sampling Oscilloscope

► 80A06 PatternSync Trigger Module



PatternSync Trigger Module

80A06 PatternSync Trigger module is required for the DSA8200 Series when using 80SJNB advanced Jitter, Noise and BER Analysis software package. When this module is used with the 82A04 Phase Reference module, the jitter floor is ≤ 200 fs_{RMS}.

The PatternSync Trigger Module is programmable to pattern lengths of up to 2^{23} bits and accepts a user-supplied clock signal from 150 MHz to 12.75 GHz. The DSA8200 UI/PI for the 80A06 module offers pattern lengths from 2 to 2^{23} bits and programs the module to within its hardware range (a pre-scaler followed by a counter with minimum count of 30) for the least-common-multiple count.

► Characteristics

Acquisition Modes –

Standard, triggered phase reference and FrameScan.

Compatible Mainframes –

DSA8200, TDS/CSA8200, TDS/CSA8000B and TDS/CSA8000.

Mainframe Resources Required –

One small (electrical) module slot or TRIGGER PROBE POWER connector on the front panel of the oscilloscope (with available SlotSaver cable).

General Specifications

Input/Output Connectors –

Precision 18 GHz SMA female connector.

Input and Output Impedance – 50 Ω .

Absolute Maximum Input Voltage – 2.0 V_{pk-pk}.

Maximum DC Offset – ± 5.0 VDC.

Input Electrical Return Loss –

>15 dB (50 MHz to 10 GHz).

>10 dB (10 GHz to 20 GHz).

Input/Output Coupling –

Clock In: AC.

Clock Out: AC.

Trigger Out: DC.

Supported Clock Rates –

Minimum: 150 MHz.

Maximum: 12.5 GHz, 12.75 GHz (typical).

Prescaler Ratios –

Input clock (as selected in the UI/PI):

150 MHz to 3.5 GHz: 4.

3.5 GHz to 7 GHz: 8.

7 GHz to 12.75 GHz: 16.

Programmable Pattern Length –

Minimum: 2.

Maximum: 2^{23} (8,388,608).

Front Panel Output Amplitudes –

Clock Out: output (50 Ω AC coupled).

150 MHz to 8.0 GHz: 500 mV_{pk-pk} (typical).

8.0 GHz to 12.75 GHz: 250 mV_{pk-pk} (typical).

Trigger Out output (50 Ω DC coupled, ground referenced).

Output High Level: 0 V.

Output Low Level: -550 mV (typical).

Front Panel Output Rise and Fall Times –

Clock Out: <60 ps (faster for fast input slew rate).

Trigger Out: <60 ps (faster for fast input slew rate).

► Features & Benefits

Enables 80SJNB – the Advanced Jitter, Noise and BER Analysis Software

Provides Trigger on Repetitive Patterns from 2 to 2^{23} Bits Long

Provides Buffered Clock Output for Input to a Phase Reference Module or Other Devices

Accepts Clock from Clock Recovery Circuits (CR) in the 80A05 and 80A07 Electrical Clock Recovery Modules and in the Optical Modules

Optional SlotSaver Adapter Provides Power and Control of the PatternSync Trigger Modules, External to the Mainframe, Saving Space for Additional Channels

► Applications

When Used in Combination with 80SJNB: Characterizing Jitter, Noise and BER Performance of High-speed Serial Designs from 1 Gb/s to 60 Gb/s Data Rates

Design Validation and Testing of Next-generation High-speed Serial Data Computer and Communications Components and Systems

When Used in Combination with 80SJNB: Jitter, Noise and BER Analysis of Multi-gigabit Standards such as Fibre Channel, OIF CEI, XFP, UXPi, SATA, PCI, Physical Layer, XAUI, Gigabit Ethernet, Rapid I/O, InfiniBand and Other Electrical or Optical Circuits

Enables Capture of Bits in a Repetitive Pattern for Bit Analysis When Only a Clock is Available for Triggering

Enables FrameScan® of Repetitive Patterns When Only a Clock Signal is Available for Triggering

*¹ Also compatible with TDS/CSA8200, TDS/CSA8000B and TDS/CSA8000 sampling oscilloscopes.

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System Jitter –

DSA8200, TDS/CSA8200 and TDS/CSA8000B with 80A06: <1.3 ps_{RMS}, 850 fs_{RMS} (typical).

TDS/CSA8000 with 80A06: <1.6 ps_{RMS}, 1.0 ps_{RMS} (typical).

DSA8200 and TDS/CSA8200 with 80A06 and 82A04: ≤200 fs_{RMS} (determined by the 82A04, see 82A04 data sheet for more details).

Minimum Input Sensitivity – 200 mV_{pk-pk}.

Environmental and Mechanical

Weight	kg	lbs
Net	0.4	0.6
Dimensions	mm	in
Height	25	1.0
Width	79	3.1
Depth	135	5.3

Environmental Conditions –

Refer to the host instrument specification.

Electromagnetic –

Refer to the host instrument specification.

► Ordering Information

80A06

PatternSync Trigger Module.

Includes: User manual, 2 each 12" SMA cables (174-1364-00).

Service Options

Opt. D1 – Calibration data reports.

Opt. R3 – Extend repair warranty to three years.

Accessories

Sampling Module Extender Cable (2-meter length) – Order 80N01.

SlotSaver Adapter Extender Cable – Brings power and control to the 80A06 when operated externally from the DSA mainframe, saving slot space (compatible with 80A06 and 80A02). Order 174-5230-00.

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Our most up-to-date product information is available at:

www.tektronix.com



Product(s) are manufactured in ISO registered facilities.

Product(s) complies with IEEE Standard 488.1-1987, RS-232-C, and with Tektronix Standard Codes and Formats.

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